APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/536,952	03/28/2000	Carl E. Clark	POU9-2000-0030-US1	5903

7590   07/17/2003

Kevin P Radigan Esq
Heslin & Rothenberg P C
5 Columbia Circle
Albany, NY   12203

EXAMINER
OPIE, GEORGE L

ART UNIT	PAPER NUMBER
2126	

DATE MAILED: 07/17/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

PTO-90C (Rev. 07-01)

	Application No.	Applicant(s)
Office Action Summary	09/536,952	Clark et al.
	Examiner	Art Unit
	George L. Opie	2126 ~~2161~~

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE _3_ MONTH(S) FROM
THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136 (a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).

Status

1) ___ Responsive to communication(s) filed on _____.

2a) ___ This action is **FINAL**.   2b) _x_ This action is non-final.

3) ___ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) _X_ Claim(s) _1-31_ is/are pending in the application.

   4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5) ___ Claim(s) _____ is/are allowed.

6) _X_ Claim(s) _1-31_ is/are rejected.

7) ___ Claim(s) _____ is/are objected to.

8) ___ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) ___ The specification is objected to by the Examiner.

10) ___ The drawing(s) filed on _____ is/are objected to by the Examiner.

11) ___ The proposed drawing correction filed on _____ is: a) ___ approved b) ___ disapproved.

12) ___ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. § 119

13) ___ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d).

   a) ___ All b) ___ Some * c) ___ None of the CERTIFIED copies of the priority documents have been:

   1. ___ received.

   2. ___ received in Application No. (Series Code / Serial Number) _____.

   3. ___ received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

   * See the attached detailed Office action for a list of the certified copies not received.

14) ___ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. & 119(e).

Attachment(s)

14) _X_ Notice of References Cited (PTO-892)
15) ___ Notice of Draftsperson's Patent Drawing Review (PTO-948)
16) ___ Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____.

17) ___ Interview Summary (PTO-413) Paper No(s). _____.
18) ___ Notice of Informal Patent Application (PTO-152)
19) _X_ Other: Text Doc for 5,361,356

U.S. Patent and Trademark Office
PTO-326 (Rev. 3-98)   Office Action Summary   Part of Paper No. _4_

DETAILED ACTION

1.   Request for copy of Applicant's response on floppy disk:
Please help expedite the prosecution of this application by including, along with
your amendment response in paper form, an electronic file copy in WordPerfect,
Microsoft Word, or in ASCII text format on a 3½ inch IBM format floppy disk.
Please include all pending claims along with your responsive remarks. Only the
paper copy will be entered -- your floppy disk file will be considered a duplicate
copy. Signatures are not required on the disk copy. The floppy disk copy is not
mandatory, however, it will help expedite the processing of your application.
Your cooperation is appreciated.

2.   The U.S. Patents used in the art rejections below have been provided as
text documents which correspond to the U.S. Patents. The relevant portions of
the text documents are cited according to page and line numbers in the art
rejections below. For the convenience of Applicant, the cited sections are
highlighted in the *text documents*. Consistent with Office procedure, the U.S.
Patents corresponding to the *text documents* are also included with this action.

3.   Claim Rejections - 35 U.S.C. § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. § 102
that form the basis for the rejections under this section made in this Office
action:

A person shall be entitled to a patent unless

(b) the invention was patented or described in a printed publication in this or a
foreign country or in public use or on sale in this country, more than one year
prior to the date of application for patent in the United States.

4.   Claims 1-31 are rejected under 35 U.S.C. § 102(b) as being anticipated
by Clark et al. (U.S. Patent 5,361,356).

As to claim 1, Clark teaches a method for producing a secure subspace for a
transaction (invention . . . protects subspace data and programs from inadvertent
accesses from other subspaces, p1 28-57) comprising:
from an operating system task (operating system dispatch program, p8 25-34)
attaching a subtask that will restrict application addressing (restrict the
addressability of each of these programs, p7 30-56) and

wherein said attaching includes defining a subspace address environment as home space (definition of a subspace group of address spaces, involving the storing of an identifier of the group, p13 12-40) within a dispatchable unit access list (DU-AL) associated with said subtask (dispatching process for a task ... contains a DUALD, Id.).

As to claim 2, Clark teaches a system of restricting "programs that are in basespace/subspace types of address spaces in a single family group", p7 30-56, and inherent in this would be the instantiation of the recited first and second tasks, subspaces and applications that implement the secure isolation operations for multiple families to execute separated transactions.

As to claim 3, Clark (p8 11-24) teaches "isolation by not providing a transaction with information for accessing its calling program in another address space."

As to claim 4, Clark (p10 7-18) teaches the "operating system ... is mapped into all address spaces it supports."

As to claims 5-6, note the claim 2 discussion supra.

> As to claim 7, Clark (p13 12-40) teaches "for each task to be done by the computer. . . several associated control blocks are generated by the operating system" including the "DU-AL 66 . . . which contain the real addresses for locating the ASTEs which respectively represent all subspaces".

> As to claim 8, Clark (abstract) teaches the "BSG instruction has an operand defining a general register containing the target virtual address and an associated access register containing an access-list-entry-token (ALET) defining the target address space.

> As to claims 9-10, note the claim 2 discussion supra.

As to claims 11-20, note the rejections of claims 1-10 above. Claims 11-20 are the same as claims 1-10 except claims 11-20 are computer program product claims and claims 1-10 are method claims.

As to claims 21-30, note the rejections of claims 1-10 above. Claims 21-30 are the same as claims 1-10 except claims 21-30 are apparatus claims and claims 1-10 are method claims.

As to claim 31, note the rejection of claim 1 above. The limitations in claim 31 are functionally equivalent to the claim 1 limitations.

5.   The prior art of record and not relied upon is considered pertinent to the applicant's disclosure. Each reference disclosed below is relevant to one or more of the Applicant's claimed invention.

U.S. Patent No. 5,745,676 to Hobson et al. which teaches the secure execution of program groups using subspace identification/allocation;

U.S. Patent No. 5,493,661 to Alpert et al. which teaches the insertion of multiple programs applying the DU-AL control for transaction isolation;

U.S. Patent No. 5,319,758 to Arai et al. which teaches the creation of subspaces and home space with respect to program groupings;

U.S. Patent No. 4,979,098 to Baum et al. which teaches the fundamental algorithm and structures in secured subspace operations for transaction efficacy.

Contact Information:

PTO Policy for Facsimile Submissions:

☐   AFTER-FINAL faxes must be signed and sent to (703) 746-7238.

☐   OFFICIAL faxes must be signed and sent to (703) 746-7239.

☐   NON OFFICIAL faxes should be sent to (703) 746-7240.

☐   All responses sent by U.S. Mail should be mailed to:
           Commissioner for Patents
           PO Box 1450
           Alexandria, VA 22313-1450

☐   Hand-delivered responses should be brought to Crystal Park Two, 2021 Crystal Drive, Arlington, VA., Sixth Floor (Receptionist). All hand-delivered responses will be handled and entered by the docketing personnel. Please do not hand deliver responses directly to the Examiner.

☐   Any inquiry of a general nature or relating to the status of this application should be directed to the Group receptionist at **(703) 305-9600.**

☐   Any inquiry concerning this communication or earlier communications from the examiner should be directed to George Opie at (703) 308-9120 or via e-mail at *George.Opie@uspto.gov.* Internet e-mail should not be used where sensitive data will be exchanged or where there exists a possibility that sensitive data could be identified unless there is an express waiver of the confidentiality requirements under 35 U.S.C. 122 by the Applicant. Sensitive data includes confidential information related to patent applications.

ST. JOHN COURTENAY III
PRIMARY EXAMINER